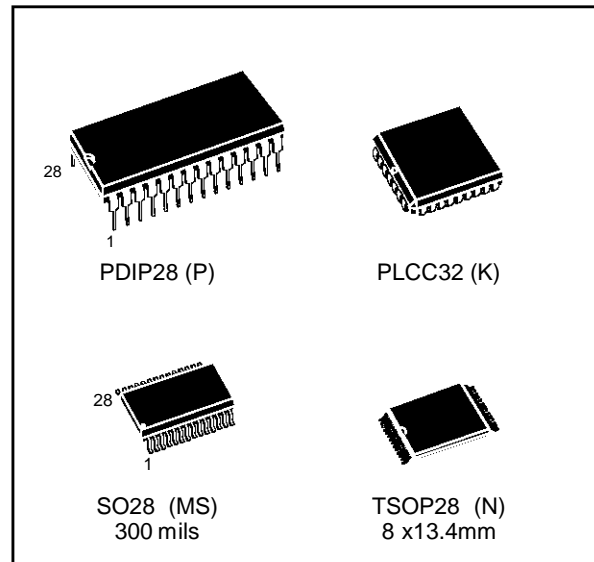


LOW VOLTAGE PARALLEL ACCESS 16K (2K x 8) EEPROM WITH SOFTWARE DATA PROTECTION

DATA BRIEFING

- FAST ACCESS TIME: 200ns
- SINGLE LOW VOLTAGE OPERATION
- LOW POWER CONSUMPTION
- FAST WRITE CYCLE:
 - 64 Bytes Page Write Operation
 - Byte or Page Write Cycle: 3ms Max
- ENHANCED END OF WRITE DETECTION:
 - Ready/Busy Open Drain Output
 - Data Polling
 - Toggle Bit
- PAGE LOAD TIMER STATUS BIT
- HIGH RELIABILITY SINGLE POLYSILICON, CMOS TECHNOLOGY:
 - Endurance >100,000 Erase/Write Cycles
 - Data Retention >40 Years
- JEDEC APPROVED BYTEWIDE PIN OUT
- SOFTWARE DATA PROTECTION



DESCRIPTION

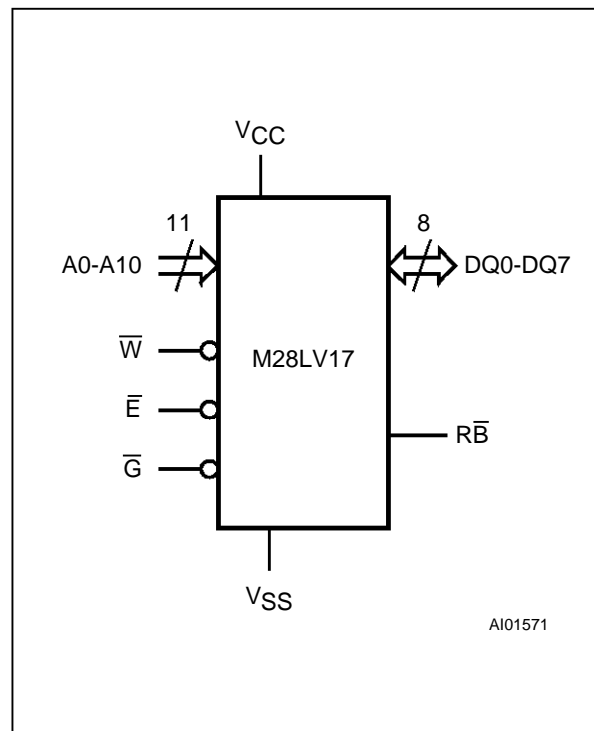
The M28LV17 is a 2K x 8 low power Parallel EEPROM fabricated with SGS-THOMSON proprietary single polysilicon CMOS technology. The device offers fast access time with low power dissipation and requires a 2.7V to 3.6V power supply.

The M28LV17 offers the same features than the M28LV16, in addition to the Ready/Busy pin.

Signal Names

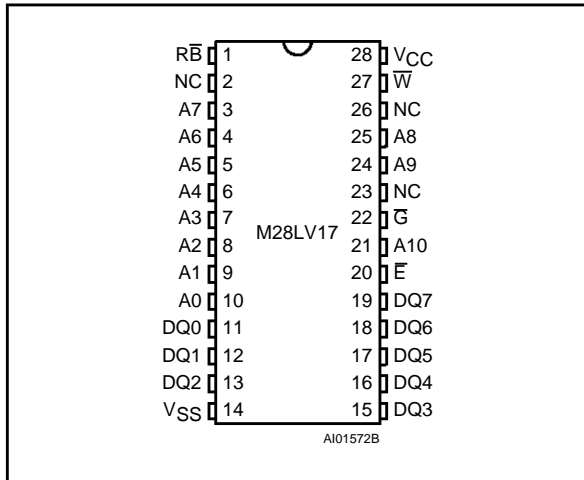
A0 - A10	Address Input
DQ0 - DQ7	Data Input / Output
\bar{W}	Write Enable
\bar{E}	Chip Enable
\bar{G}	Output Enable
$\bar{R}\bar{B}$	Ready / Busy
V _{cc}	Supply Voltage
V _{ss}	Ground

Logic Diagram



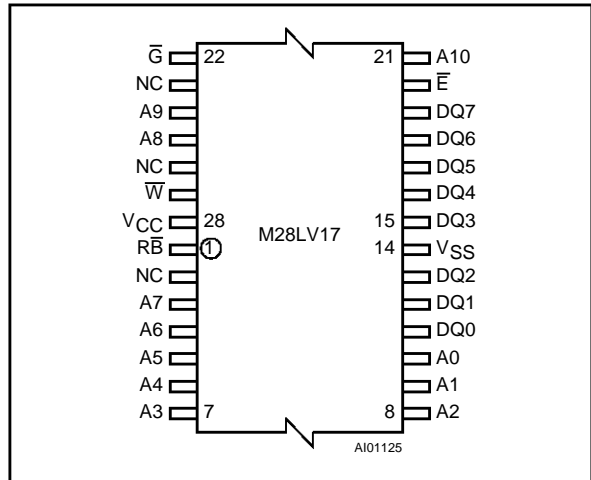
M28LV17

DIP Pin Connections



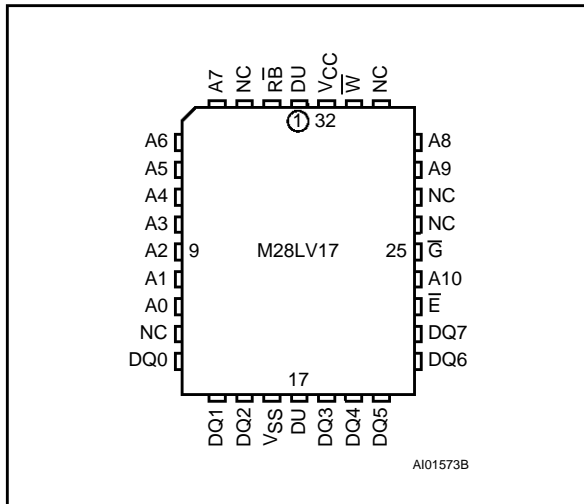
Warning: NC = Not Connected.

TSOP Pin Connections



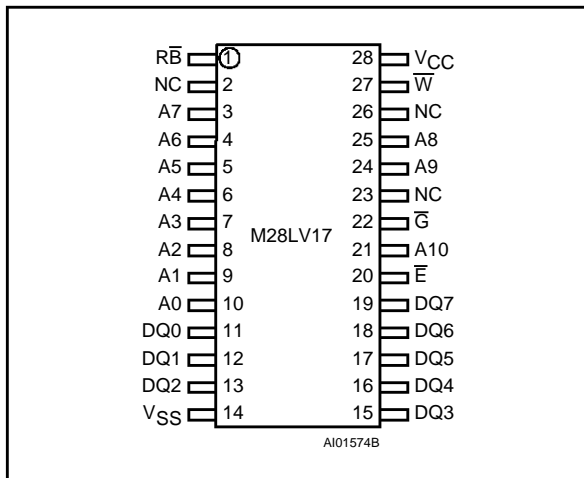
Warning: NC = Not Connected.

LCC Pin Connections



Warning: NC = Not Connected, DU = Don't Use.

SO Pin Connections



Warning: NC = Not Connected.

Ordering Information Scheme

For a list of available options refer to the current Memory Shortform catalogue.

For further information on any aspect of this device, please contact the SGS-THOMSON Sales Office nearest to you.

Example: M28LV17 -200 K 1

Speed	Package	Temp. Range
-200 200ns	P PDIP28	1 0 to 70 °C
-250 250ns	K PLCC32	6 -40 to 85 °C
-300 300ns	MS SO28 300mils	
	N TSOP28 8 x 13.4mm	